

Electronic Acknowledgement Receipt

EFS ID:	1100108
Application Number:	10759483
Confirmation Number:	7587
Title of Invention:	Microprocessor with variable latency stack cache
First Named Inventor:	Rodney E. Hooker
Customer Number:	23669
Filer:	James Weldon Huffman/Taysie Locke
Filer Authorized By:	James Weldon Huffman
Attorney Docket Number:	CNTR.2232
Receipt Date:	30-JUN-2006
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Application Type:	Utility
International Application Number:	

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Information Disclosure Statement (IDS) Filed	CNTR_2232_IDS.pdf	130741	no	3

Warnings:**Information:**

This is not an USPTO supplied IDS fillable form

2	Foreign Reference	CN01391167A_.pdf	65253	no	1
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Warnings:**Information:**

Total Files Size (in bytes):	195994
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.